

NFD315

Differential to Single Ended 4 to 20 Decoder

1. Device Features

- Differential LVDS input
- CMOS compatible output
- VDD: +3.3 – 5 V
- Also available in bare die form
- <10 ns rise / fall time at typical load
- <10 ns propagation delay
- Quiescent current: 3 mA

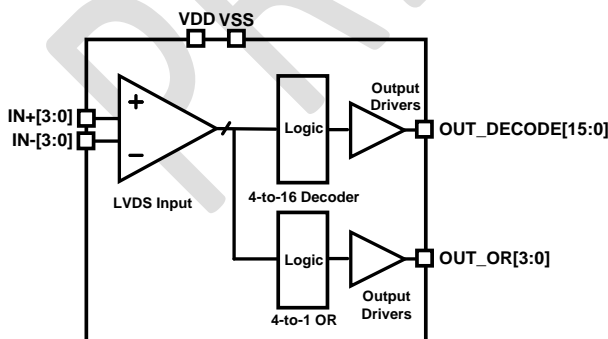
2. Device Applications

- Decoding for general purpose control

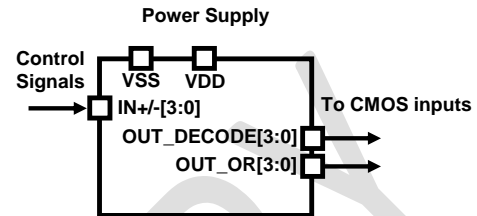
3. Device Description

The NFD315 is a 4 differential input 20 single ended output decoder. Acting as a 4-to-16 decoder, 1 of the output is at logical low (0 V) at any given time, while the other 15 are at logical high (VDD). The other 4 outputs act as a quadrant indicator where 1 of them is at logical low (0 V) at any given time while the other three are at logical high (VDD).

4. Simplified Block Diagram

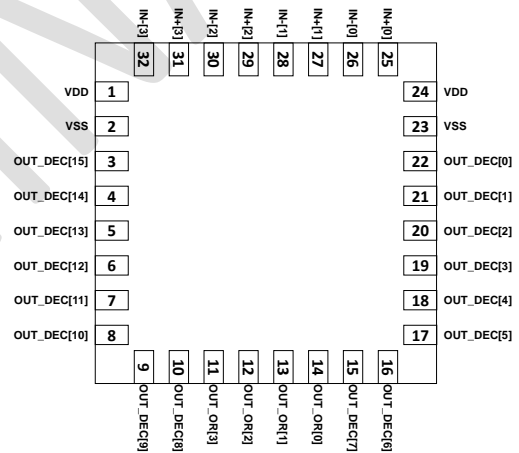


5. Simplified Application Diagram



6. Chip Description (Packaged)

NFD315 comes in a packaged form factor of QFN32 5x5 mm at 0.5 mm pitch. For smaller form factors, please inquire at hello@noisefigure.com.



7. Chip Description (Bare Die)

